

Next-Generation Semiconductors Signature Symposium

Thursday, Aug 6 10:15 – 12:00pm ChampionsGate

Imagining Tomorrow’s Semiconductor Technologies

The Signature Symposium on Semiconductors will explore emerging trends, opportunities, and challenges shaping the future of semiconductor technologies and the global semiconductor ecosystem. The forum aims to bring together experts from academia, industry, government, and research organizations to exchange ideas on innovation, manufacturing, advanced materials, packaging, supply chains, workforce development, and future applications enabled by semiconductor technologies. Through invited talks and a panel discussion, the forum seeks to encourage collaboration, foster innovation, and promote strategic partnerships that will help advance the next era of semiconductor research, development, and commercialization.

Chair



Jin W. Choi

President of KEEE
Professor of Electrical and
Computer Engineering
Michigan Technological
University

Speakers



Jinho Ahn

Executive Vice President
for Research,
Hanyang University



Dae-Seok Byeon

President and CEO, Korea
Advanced Nano Fab
Center, KANC



Rockwell Hsu

Technical Leader,
Engineering,
Cisco System, Inc

Time	Title and Speaker
10:15	Welcoming Remarks and Introduction Jin W. Choi (Symposium Chair)
10:20	From Imagination to Reality: The EUV Lithography Journey through Open Innovation Jinho Ahn (Executive Vice President for Research, Hanyang University)
10:45	Innovation and Imagination of Compound Semiconductors Accelerating the Penta-Singularity Era Dae-Seok Byeon (President and CEO, Korea Advanced Nano Fab Center, KANC)
11:10	Meeting System Requirements for High-Speed Signaling in Scalable AI Data Centers: Design Challenges and Manufacturing Considerations Rockwell Hsu (Technical Leader, Engineering, Cisco Systems, Inc.)
11:35	Panel Discussion Moderator: Jin W. Choi

*Please be aware that the schedule mentioned above is subject to change based on unforeseen circumstances.